

# Electronic Filing System (EFS) Data Electronic Patent Application Submission USPTO Use Only

EFS ID:

62795

Application ID:

10810035

NON-VOLATILE TRANSISTOR MEMORY ARRAY

Title of Invention:

INCORPORATING READ-ONLY

**ELEMENTS WITH SINGLE MASK** 

**SET** 

First Named Inventor:

**Albert Weiner** 

Domestic/Foreign Application:

**Domestic Application** 

Filing Date:

2004-03-26

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Statement

Filing Type:

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8458

Attorney Docket Number:

ATM-291

**Total Fees Authorized:** 

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#### **TRANSMITTAL**

Electronic Version v1.1 Stylesheet Version v1.1.0

> Title of Invention

NON-VOLATILE TRANSISTOR MEMORY ARRAY INCORPORATING READ-ONLY ELEMENTS WITH SINGLE MASK SET

Application Number:

10/810035

Date:

2004-03-26

First Named Applicant: Mr. Albert S. Weiner

Confirmation Number:

8458

Attorney Docket Number: ATM-291

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Submitted by:	Elec. Sign.	Sign. Capacity
Mr. Thomas Schneck Registered Number: 24518	/thomas schneck/	Attorney

Documents being submitted

Files

us-ids

ATM291.IDS-usidst.xml

us-ids.dtd

us-ids.xsl

Comments



#### **ELECTRONIC INFORMATION DISCLOSURE STATEMENT**

Electronic Version v18 Stylesheet Version v18.0

> Title of Invention

NON-VOLATILE TRANSISTOR MEMORY ARRAY INCORPORATING READ-ONLY ELEMENTS WITH SINGLE MASK SET

Application Number:

10/810035

Confirmation Number:

8458

First Named Applicant:

Albert Weiner

Attorney Docket Number: ATM-291

Art Unit:

2818

Search string:

(6396767).pn.

### **US Patent Documents**

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
	_ 1	6396767	2002-05-28	Denny Tzeng et al.	B1	365	230.06

## Signature

Examiner Name	Date		